Using as a Metro M0, so restrictions

* Only get the GPIO’s used on the Metro
* Analog Inputs can be used as Digital I/O on Metro
* Want to keep as much as same for debug purposes
* CAN use internal pull ups & should be available for pull downs
* Can source 7 mA

Design Points

* USB Data+ at PA25 and USB Data- at PA24
* 1 uF Cap at VddCore
* 0.1 uF for VDDIO
* 1 uF and 0.1 uF for VDDANA
* Reset should go to pushbutton w/ 0.1 uF cap and pull-up

Pinouts

A3 is Volume 1

A4 is Volume 2

A10 (Digital 8) will be Volume knob 3

A11 (Digital 9) will be Volume knob 4

**Programming Notes**

* Had issues with the crystal
* Managed to run crystalless
  + (not sure if necessary) add #define CRYSTALLESS 1 to the bootloader configuration file in uf2 bootloader
  + Rebuild and flash bootloader as necessary
    - Might not be necessary because bootloader should run cyrstalless anyway
  + Add “-DCRYSTALLESS” to the build flags for metro m0 in the boards.txt file, which is at C:\Users\Ahkeel Mohideen\AppData\Local\Arduino15\packages\adafruit\hardware\samd\1.6.6
    - This tells compiler to compile sketch as crystalless otherwise it will hang as it waits for crystal